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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/660,709 Confirmation No. 7653
Applicant : Y. KAWAMURA et al
Filed : September 12, 2003
Title : METHOD FOR POLISHING SURFACE OF SEMICONDUCTOR DEVICE SUBSTRATE
TC/AU : TBD
Examiner : TBD
Docket No. : ASA-955-02
Customer No.: 24956

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR STATUS

Sir:

Kindly advise the undersigned of the status of the above-identified application.

Your prompt attention to this matter would be appreciated.

Respectfully submitted,

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

By _____
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September 12, 2005